

What is new in the world of space electronics?

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Head of Studies

we think electronics.dependable

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DSI Aerospace Technologie GmbH is an SME located in Bremen, Germany which provides following electronic equipment:

Platform &
Instruments
Computers



Payload Data
Handling
Units (incl.
MMBs)



Test
Systems
(EGSE)

Data
Processing
Units

Engineering
Services

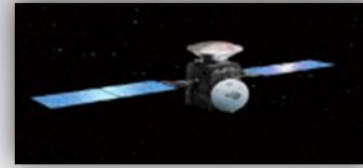
Aerospace Electronics
since 1997

A few recent projects...



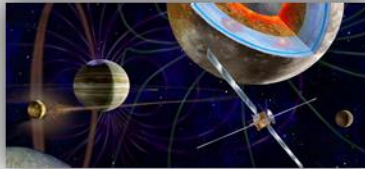
Hayabusa-II MASCOT

- On Board Computer
- Status: In Orbit



ExoMars

- Control and input/output modules incl. BSP of the Payload Data Handling Unit
- Status: In Orbit



JUICE CDMS SSMM

- Solid state mass memory board
- Status: PFM delivered



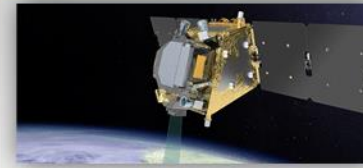
MetOp-SG ICI

- Command & data processing unit of the Ice Cloud Imager instrument
- Status: EQM delivered, PFM under manufacturing



Biomass

- Payload data handling unit
- Status: EM delivered, EQM under manufacturing



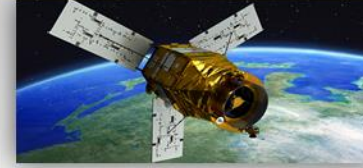
FLEX

- Payload data handling unit
- Status: under development



PLATO CDMS SSMM

- Solid state mass memory board
- Status: FuMo delivered

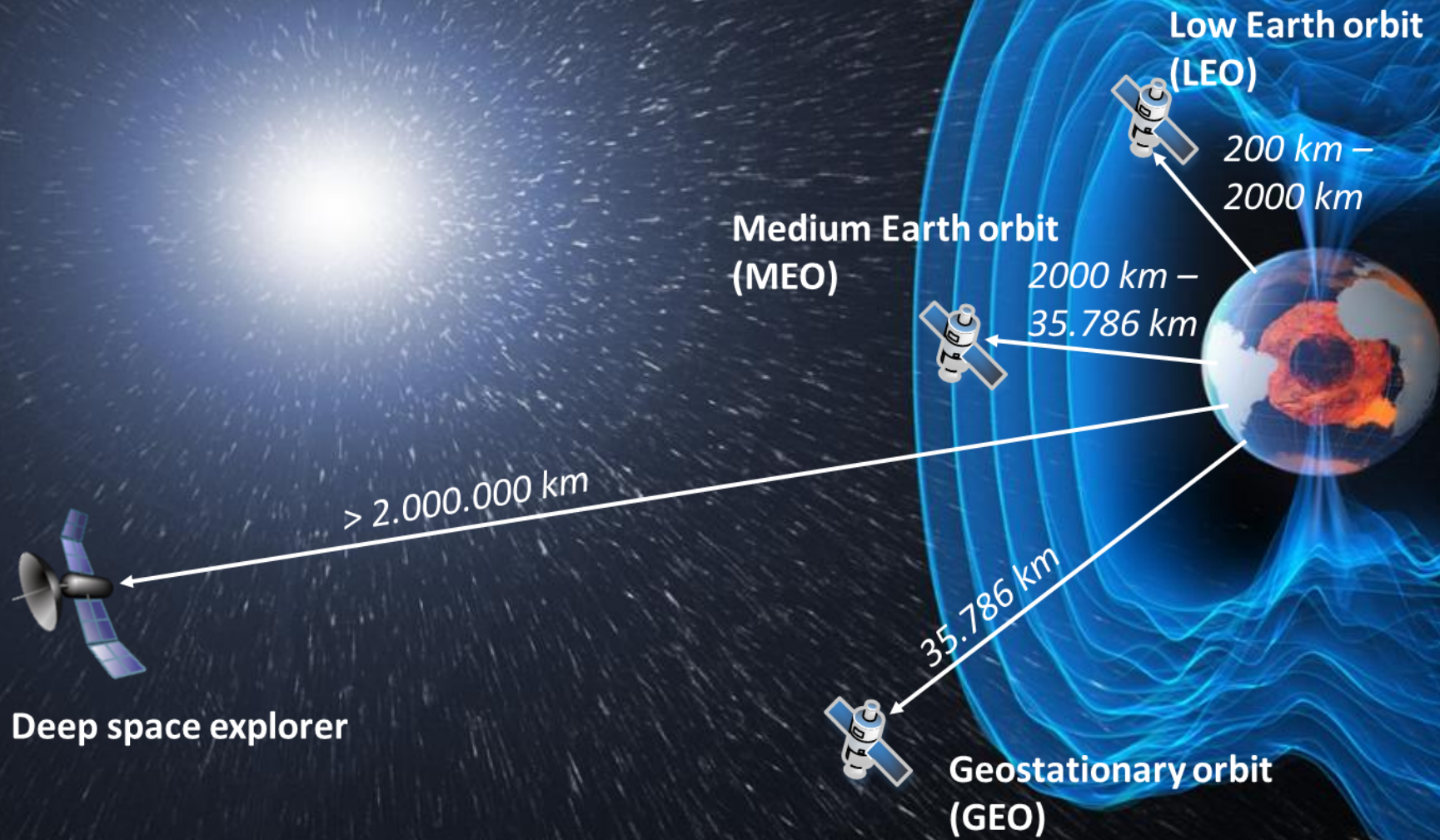


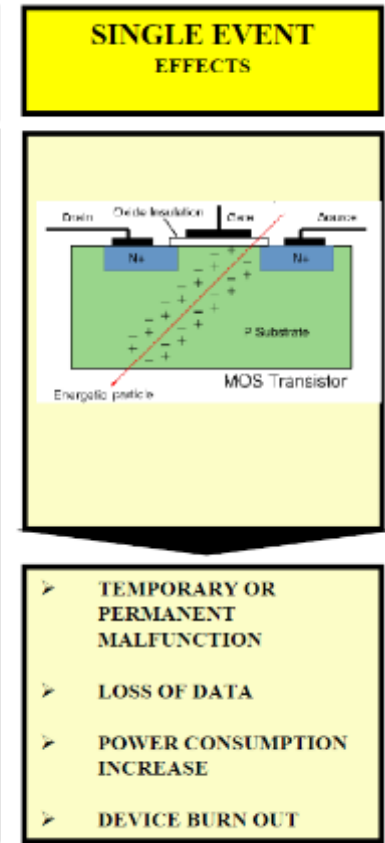
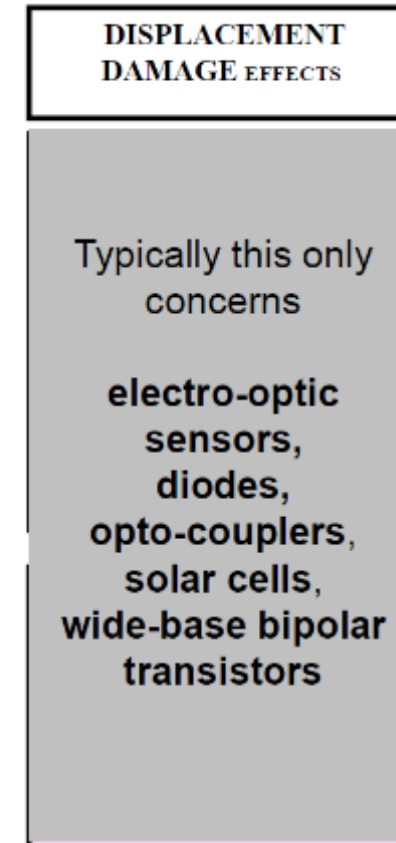
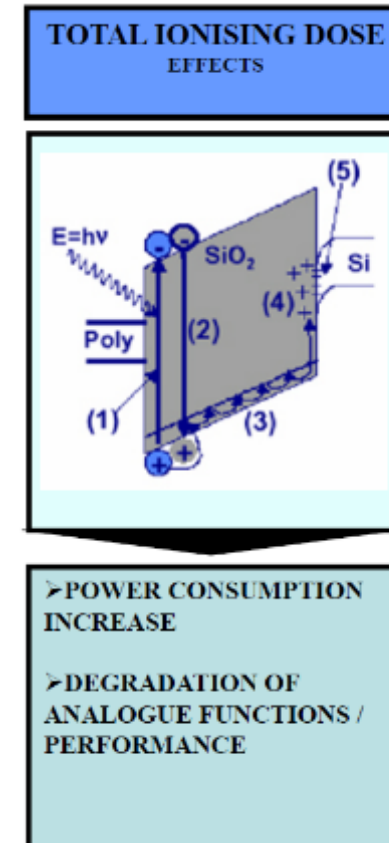
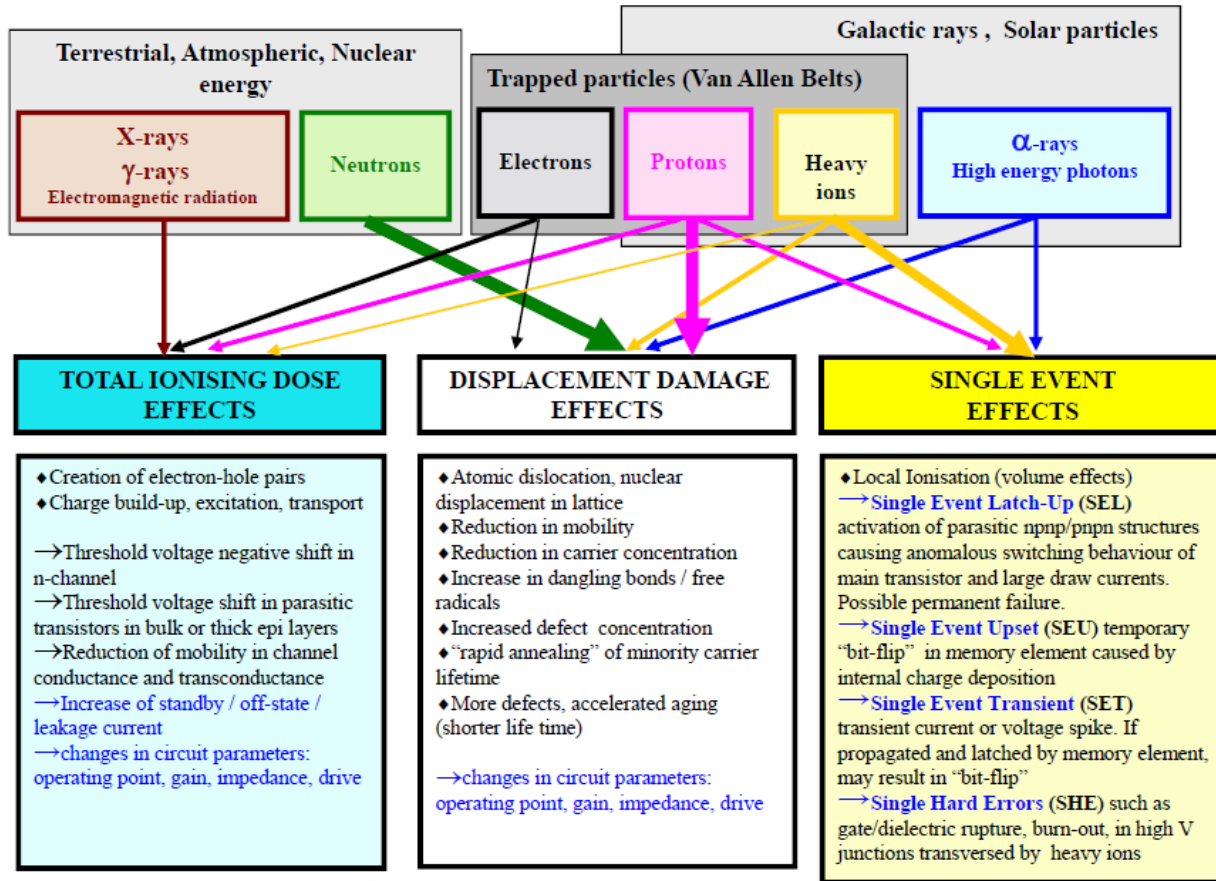
KOMPSAT-7

- Data storage and compression equipment
- Status: EQM delivered, PFM under manufacturing

- **A brief introduction to space electronic**
 - Design challenges
- **OldSpace versus NewSpace**
- **Trends in space electronics**
- **Resume**

Why is the design of space electronics somehow difficult?





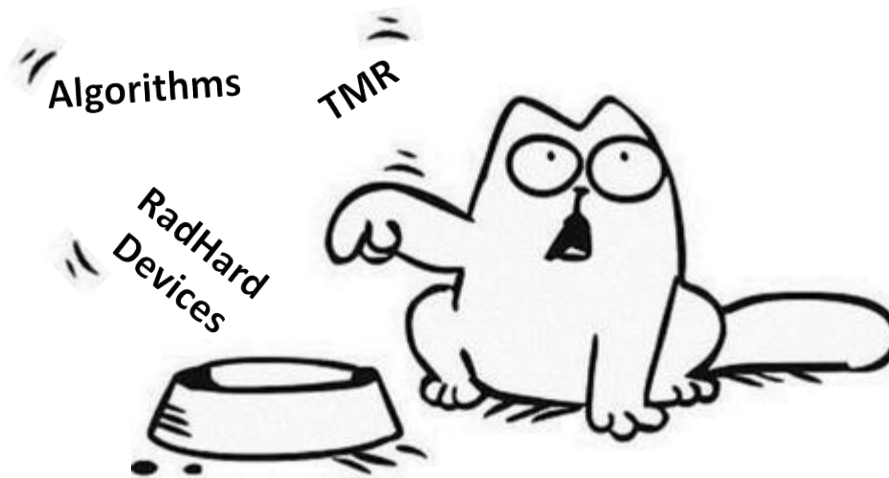
¹Source: Courtesy of ESA

⇒ Qualification is indispensable

Three common ways to deal with radiation impacts to electronic systems in space

1. **Radiation hardened (by design) devices** that can operate in the planned environment
2. **Redundancy** that considers fault-tolerance, e.g. 3 copies of each functional box and a voting system
3. Extensive **Error Detection and Correction** algorithms

Normally, we consider a combination of each of these methods

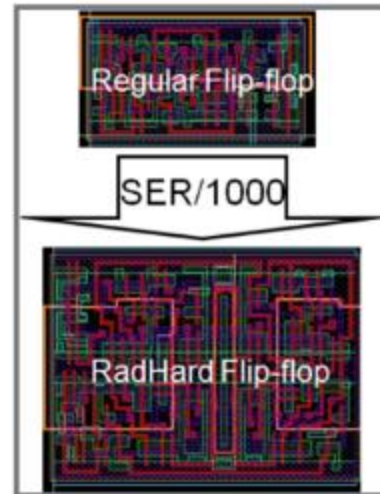
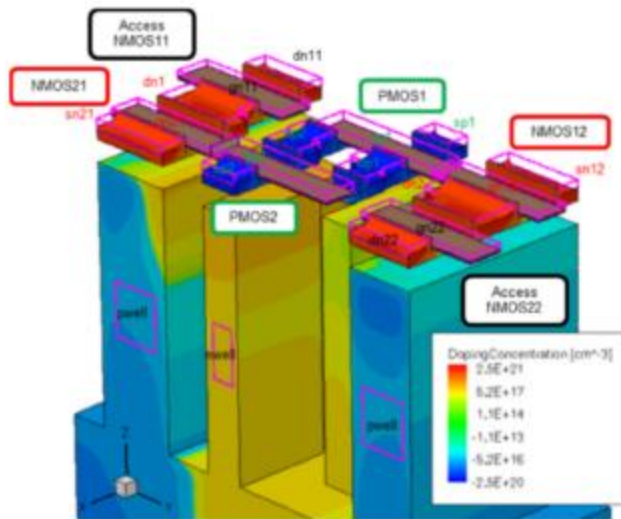


Commercial



Space-grade

Most important: Classical Space Electronics are composed of rad-hard and qualified components

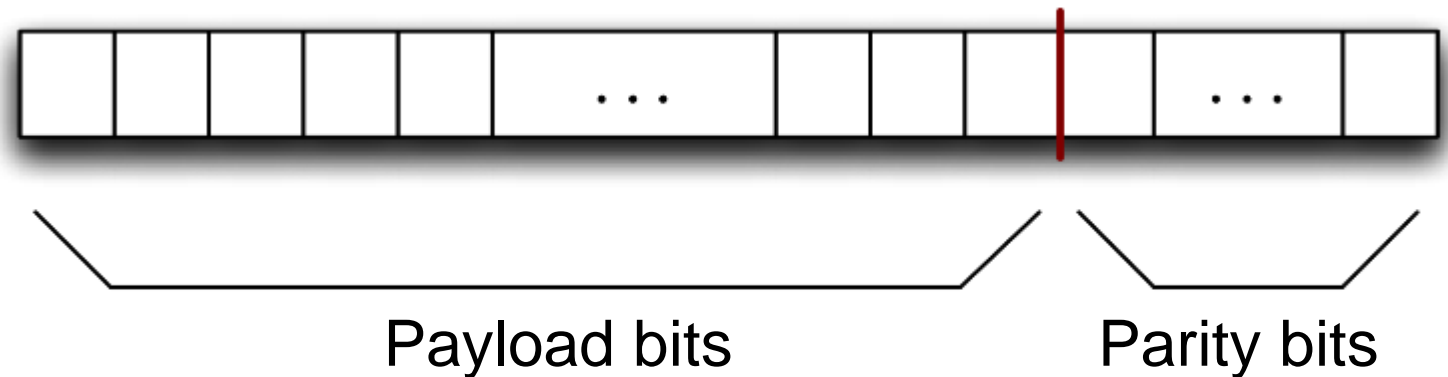


- Coarse-grained technology nodes
 - Lower frequencies (high propagation delay)
 - Higher power dissipation (heat)
- Huge footprints
- Extremely expensive
- Examples:
 - FPGA: Microchip RTAX, Xilinx Kintex Ultrascale
 - MPU: CAES GR740 Quad-Core LEON4
 - SoC: nanoXplore NG Ultra

We can not only detect errors... we can also correct them

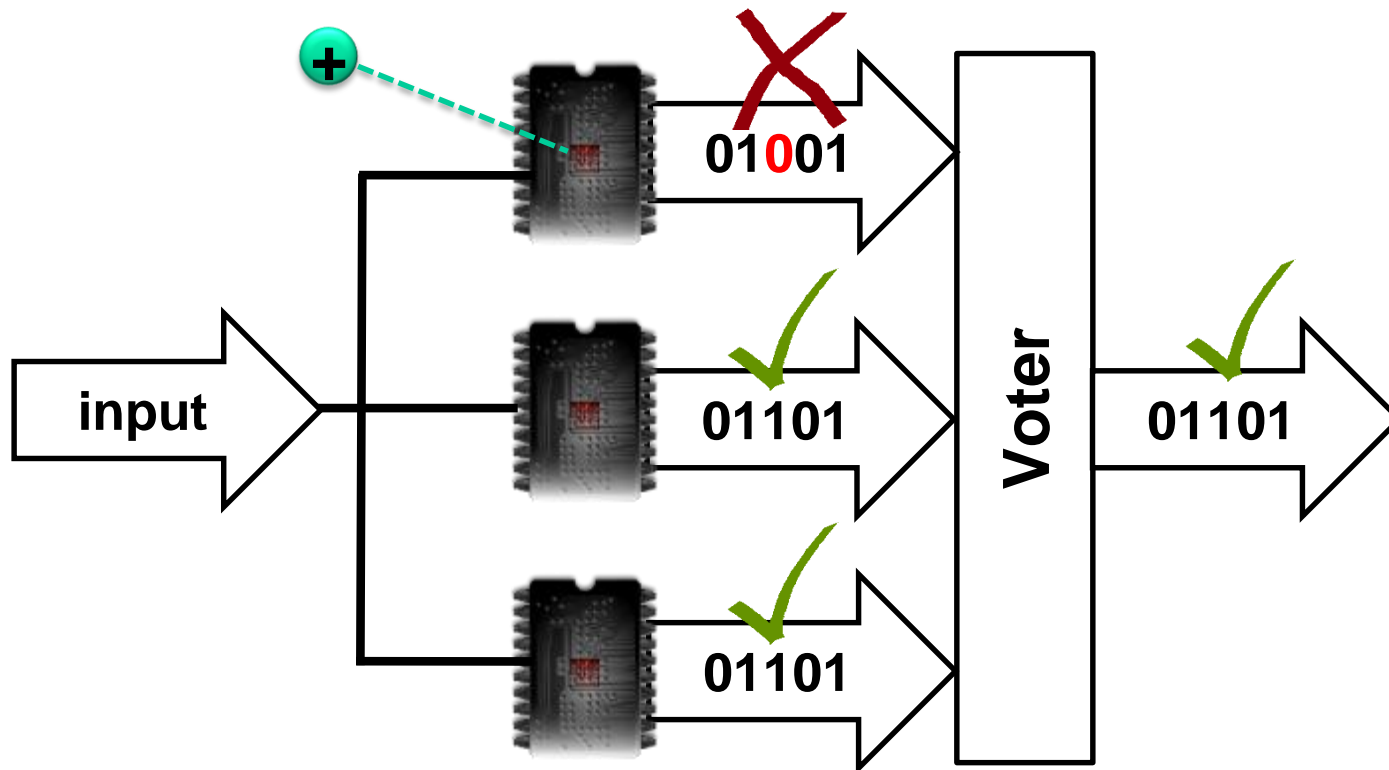
- Widely used e.g. in channel coding
- We can include Parity-Checks
- Commonly used are Reed-Solomon Codes (or Hamming codes)

Symbol	Code
A	10001
B	00100
C	01010



In order to increase reliability and fault tolerance, we can include redundancy

- Common technique is TMR/ → Triple Modular Redundancy/Dual Modular Redundancy (with parity)



TMR can be applied on **system level**, **component level** or **block level**... or even Software

- However....
 - Increased overhead (three times)
 - More power consumption
 - More complexity (weight)
 - New failure models (e.g. is the voter correct???)

Redundancy further increases availability/reliability

Paradigm shift for established space industry (and its implications)

- Exploitation of commercial-of-the-shelf (COTS) products for LEO
→ Mainly for communication purposes
- Existing, evolving constellations e.g. Starlink, OneWeb2, Telesat, Amazon, ...
→ Up to 30.000 CubeSats
→ Commercial interest far beyond classical agency driven missions

Traditional Space

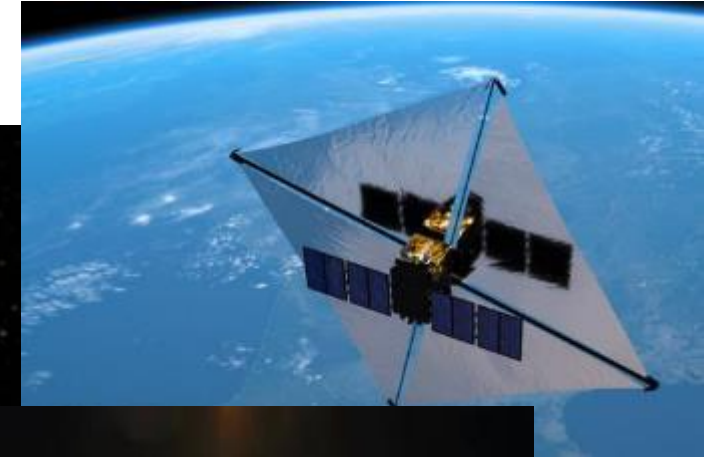


NewSpace



Cost
pressure

- Collisions lead to avalanche effect and uselessness of orbits
- Regulations required, as commercial interests > care taking (littering)



Solutions available: Who pays the bill?

- Lifetime of COTS is critical

		Environment		
		LEO Equatorial	LEO Polar (Sun Sync)	GEO / Interplanetary
Mission Lifetime (With Assumed Risk Acceptance)	> 3 Years	Data on all SEE for critical parts, and have data on dose failure distribution on similar parts	Consider mission consequences of all SEE (Data for critical parts), have Dose failure distribution on lot	Have Data on all SEE, Have Data Dose failure distribution on lot
	1-3 Years	Have Data on DSEE for critical parts	Consider mission consequences of all SEE (Data for critical parts), have data Dose failure distribution on similar parts	Have Data on all SEE for critical parts, Have Data on Dose failure distribution on similar parts
	< 1 Year	Look for data on DSEE for critical parts	Consider mission consequences of all SEE, and look for data on dose failure distribution on similar parts	Consider mission consequences of all SEE, and have data on dose failure distribution on similar parts



- Higher computational performance, fast time to market
- **If hardware costs are reduced extensively, we can just send more satellites (yep)**

Trends in (traditional) space electronics



Non-terrestrial Networks

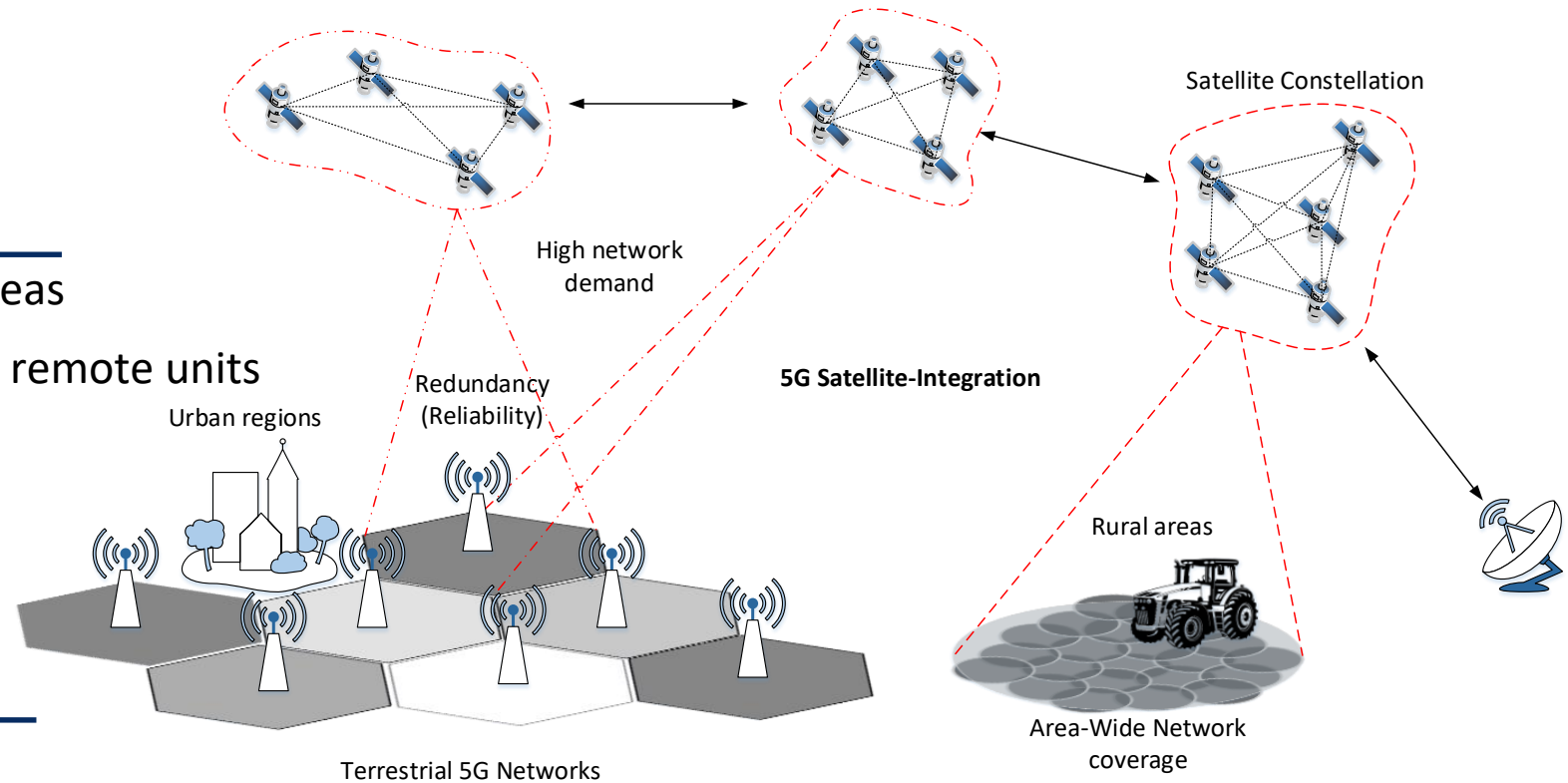
- Basic coverage of rural areas
- Satellite constellations as remote units
- Modes

- U(LL)RC
- mMTC



Non-terrestrial Nodes

- 3D networks
- Satellites as base station
- Resilient communication



Key features

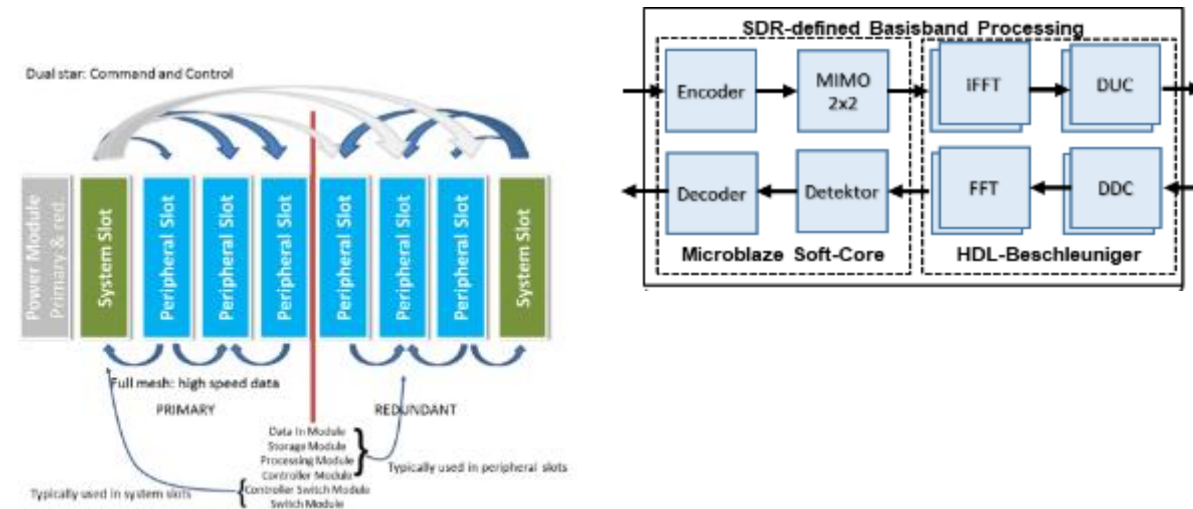
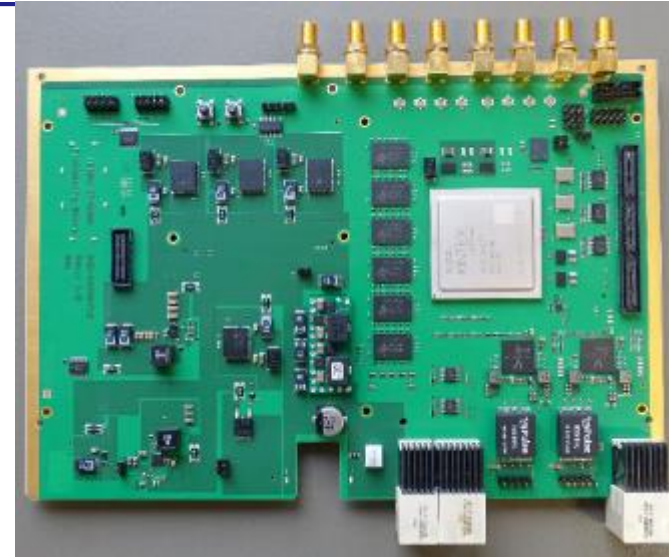
- Kintex UltraScale for Space Applications
- Various interfaces/communication protocols available

Interfaces

- Frontpanel: HSSL-based connectors
- Backplane: Compact PCI Serial Space (cPCI-SS)
- 2 Mezzanine connectors for expandability

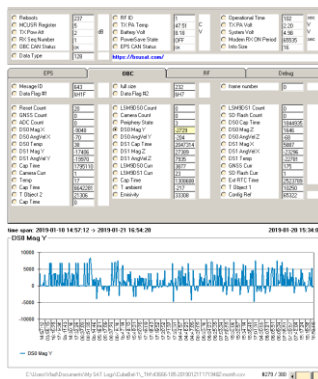
Mechanical properties

- Form factor: 6U
- Mass: < 800g (without mezzanine extension)

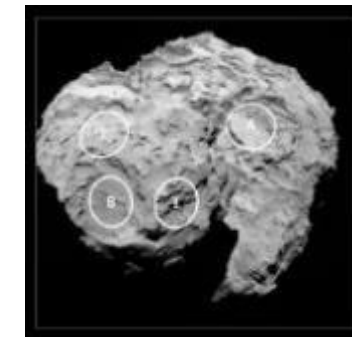


Impact of AI for Space Applications

Health Monitoring of Spacecrafts



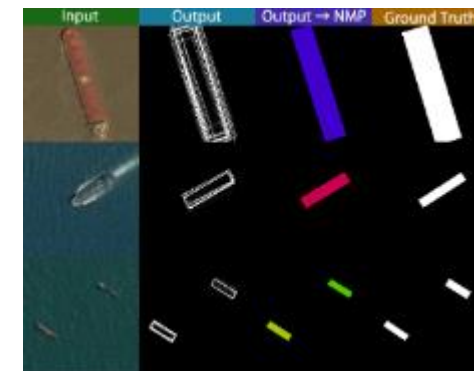
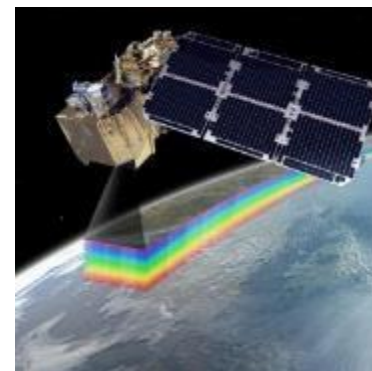
Autonomous Navigation



Communication Satellites



Feature detection for planetary exploration



Architectural design

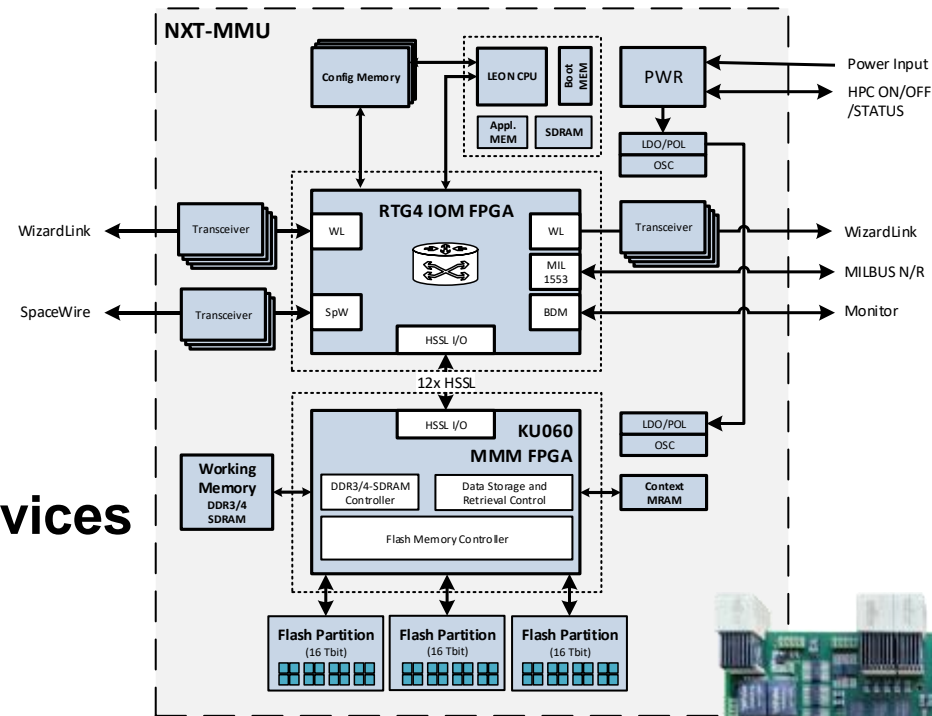
- Storage Capacity **48Tbit** BOL
- Bandwidth up to 20Gbps (recording and playback)
- Power consumption ~20W
- Mass < 20kg
- Form factor < 2x 6U

Selection and up-screening of suitable Flash devices

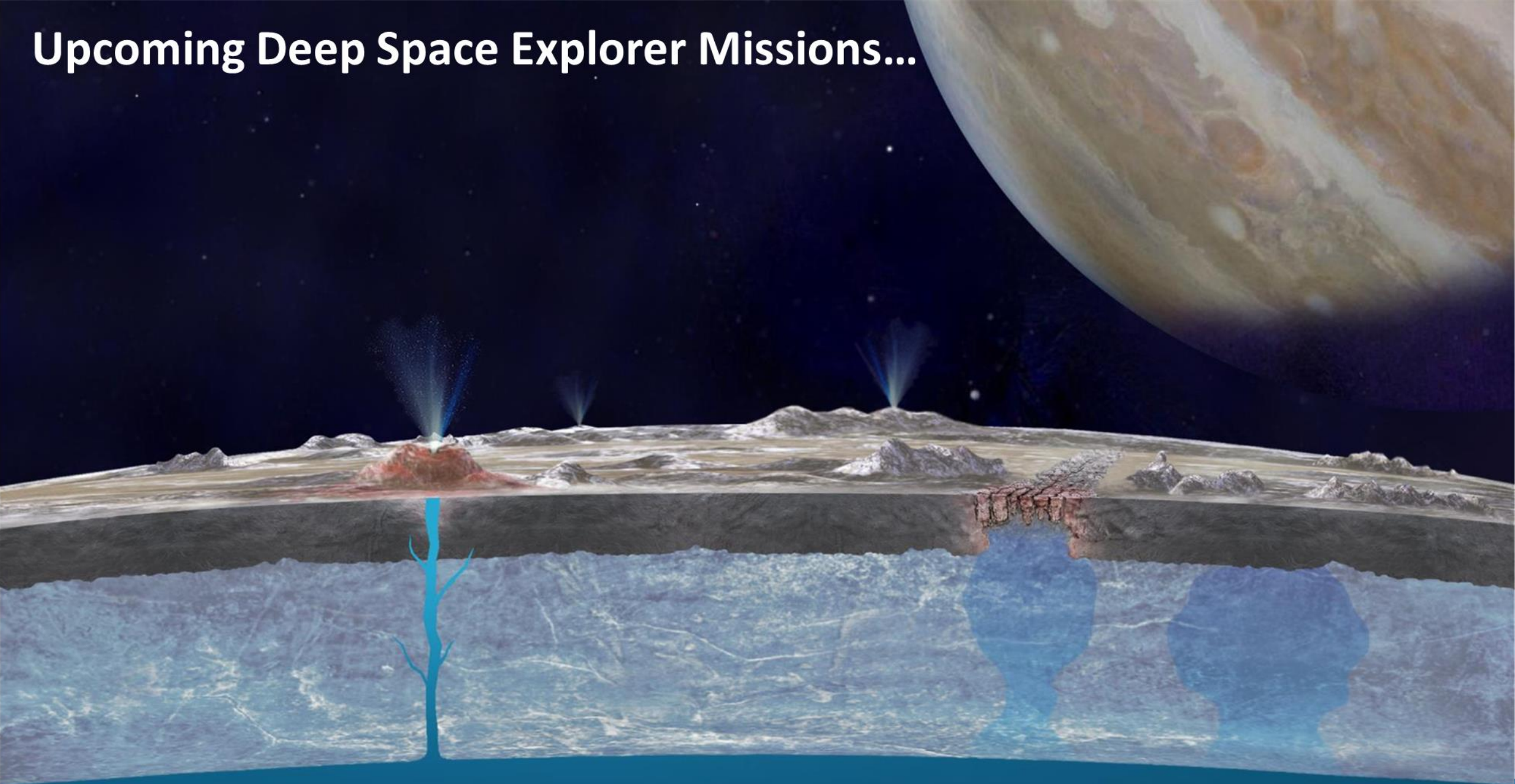
- Comprehensive NAND flash technology examination
- Up-screening

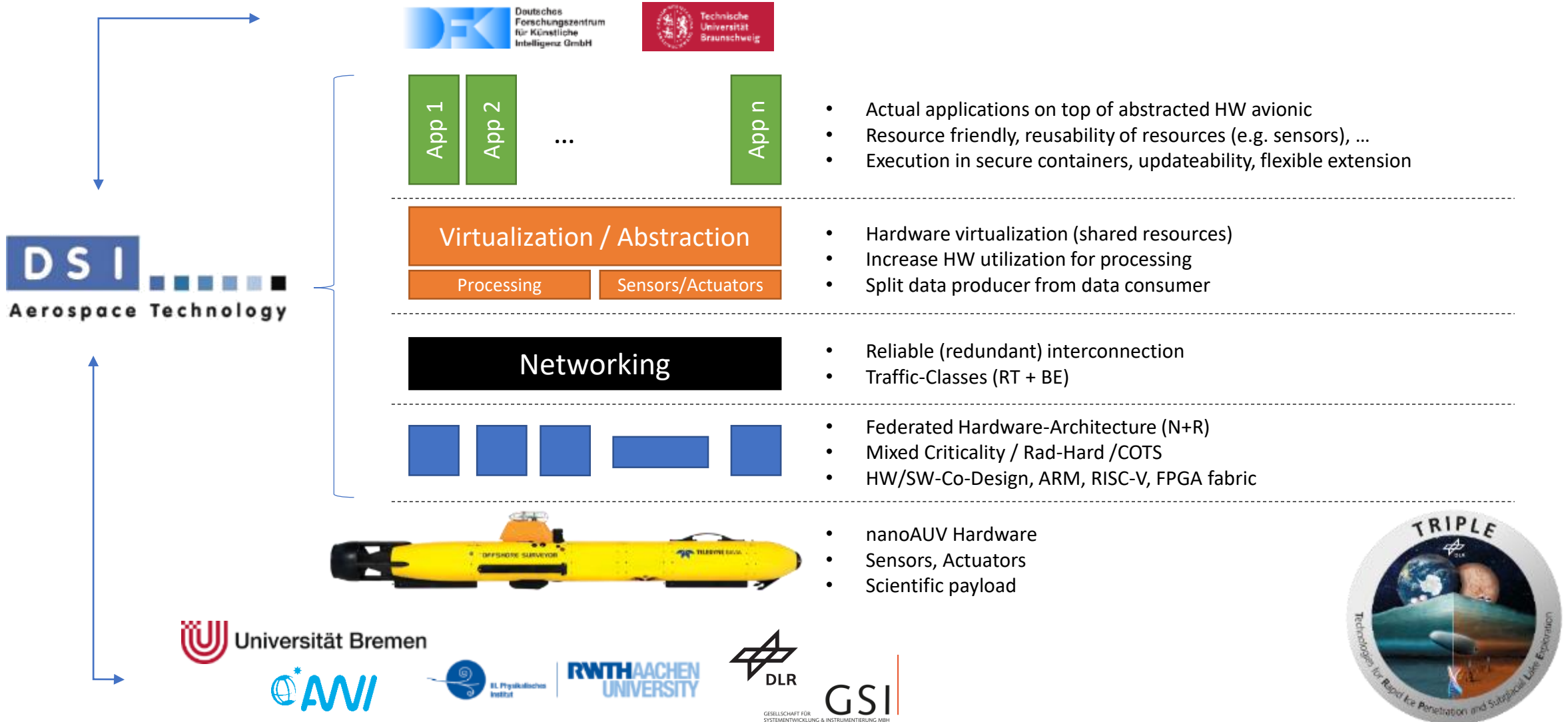
PCB manufacturing

- Comprehensive NAND flash technology examination



Upcoming Deep Space Explorer Missions...





- Actual applications on top of abstracted HW avionic
- Resource friendly, reusability of resources (e.g. sensors), ...
- Execution in secure containers, updateability, flexible extension

- Hardware virtualization (shared resources)
- Increase HW utilization for processing
- Split data producer from data consumer

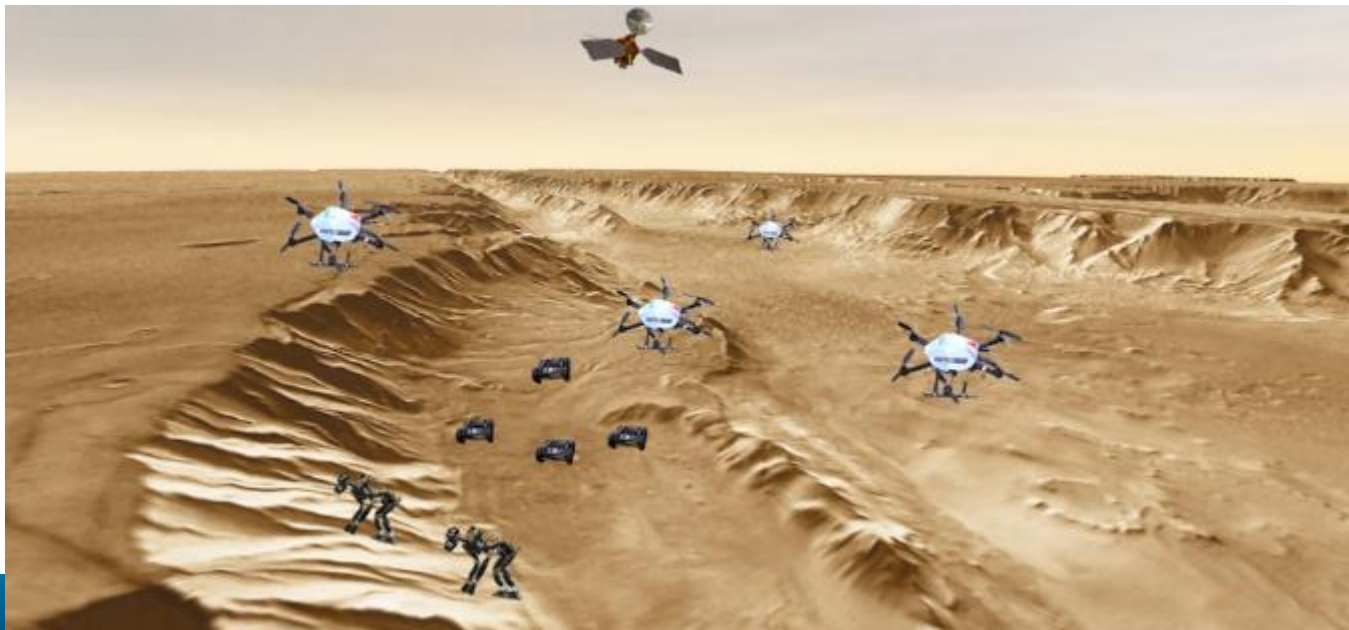
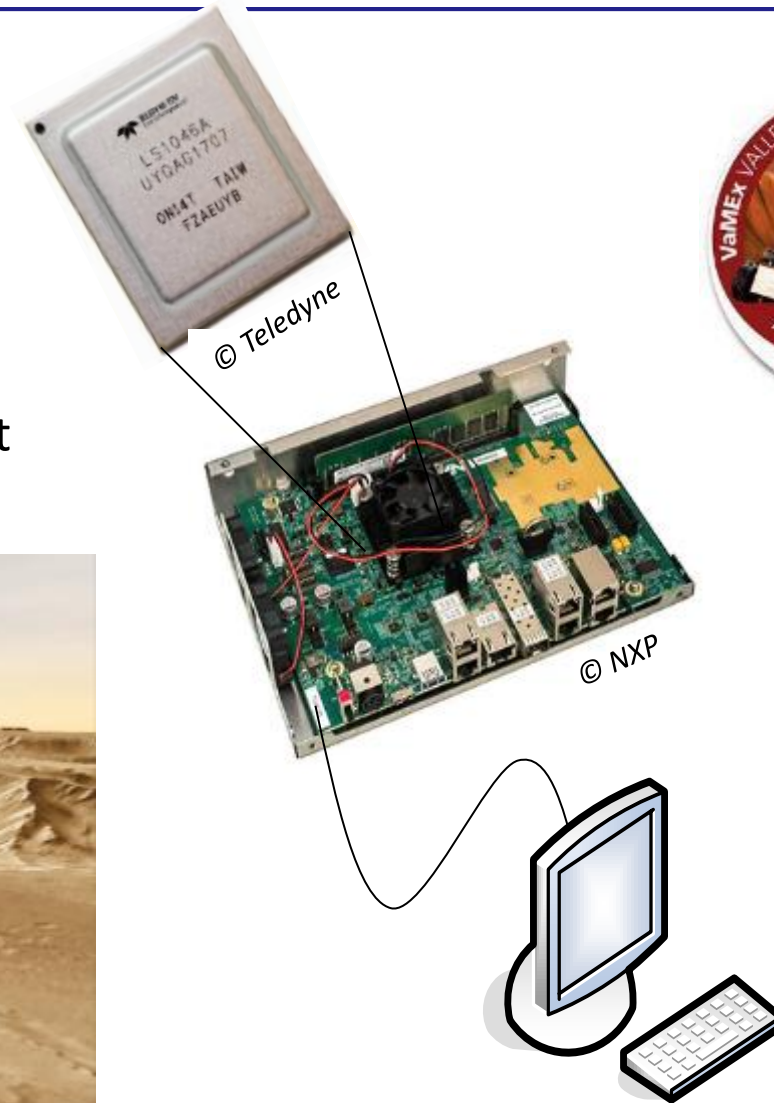
- Reliable (redundant) interconnection
- Traffic-Classes (RT + BE)

- Federated Hardware-Architecture (N+R)
- Mixed Criticality / Rad-Hard / COTS
- HW/SW-Co-Design, ARM, RISC-V, FPGA fabric

- nanoAUV Hardware
- Sensors, Actuators
- Scientific payload

Development of a Space-Grade processor platform

- Core unit: Teledyne LS1046-Space
- Quad ARM core (Cortex A72)
- Suitable for various applications
 - Image processing
 - LIDAR
- PCB development, manufacturing, validation and test
- Rapid Prototyping



electronics?

What is new in the world of space electronics?

- The battle NewSpace vs Traditional Space is still ongoing
 - High performance only possible when utilizing COTS parts, but those tend to faulty behavior in space
 - Agencies are getting more and more open-minded for COTS devices
 - NewSpace will face some serious issues
- Modern applications steadily raise the performance demands of space-grade-boards and -devices
 - Novel Generation of RHBD devices, e.g. FPGAs or SoCs, will decrease the commercial and non-terrestrial gap
 - Power/Thermal dissipation
- AI is key enabler for various space-applications
 - How to efficiently perform on-board ML?

DSI will provide different space-grade PCBs to compete with these challenging goals



Thank you for your attention!

Questions?

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